

## **REMARKS/ARGUMENTS**

Claims 1-26 remain canceled without prejudice, claims 27- 47 remain pending.  
Claims 27 and 34 are amended, as recited hereinabove.

Claims 27-29, 31-33, 41-43 and 45-47 have been rejected under 35 U.S.C. 103(a) as being allegedly unpatentable over Ozcelik (US Patent No. 5,928,321) (hereinafter referred to as "Ozcelik") and Bublil (US Patent No. 6,012,137) (hereinafter referred to as "Bublil") in view of Chen (US Patent No. 6,473,864) (hereinafter referred to as "Chen") and Mohamed et al. (US Patent No. 5,978,838) (hereinafter referred to as "Mohamed"). The foregoing rejection is respectfully disputed, as it is believed that the claimed invention is patentable in light of the cited references.

For example, on Page 2 of the Office Action, it is stated that Ozcelik substantially teaches the invention as claimed and certain references are made to Ozcelik as the basis thereof. However, unlike the claimed invention, which is directed to hardware assisted context switching for audio and video encoder/decoder, Ozcelik discloses a reduced-instruction set CPU programmed to provide software-controlled task management, a stack and to manage virtual instruction memory. [See Ozcelik: Abstract]. Furthermore, the claimed structure is not disclosed by Ozcelik, contrary to that which is stated in the Office Action. Among other differences, in Ozcelik, "code random access memory", as in the claimed invention, are not disclosed. The "data memory 152" is not a data memory, rather 152 is an allocated region, in the DRAM memory 104, for storing a stack for storing and it is not "coupled to the processing unit", as in the claimed invention. [See Ozcelik: Col. 8, lns. 23-25]. Furthermore, 112 in Ozcelik is a register file and not coupled to the processing unit.

As to Mohamed, it uses two processors, i.e. processors 110 and 120 so it cannot have the register groups coupled to the same processor, as in the claimed invention. [See Mohamed: Col. 6, lns. 37-45].

As to Chen, change in the clock rate is changed based on whether the performance critical portion of the code or a non-critical portion of the code. [See Chen: Col. 2, lns. 52-


57]. This is not the partitioning of resources for time-critical and non-time-critical tasks to perform hardware-assisted context switching, as in the claimed invention.

Moreover, none of the foregoing references hints at, suggests or teaches the other and therefore their combination does not render the claimed invention unpatentable. Furthermore, the combination of the references does not solve the problem solved by the present invention as to audio and video encoder/decoders using hardware-assisted context switching, in the manner claimed. None of the cited references are directed to solving the problem of the present invention and directed to audio and video encoder/decoder performing hardware-assisted context switching. It is therefore believed that claims 27, 34 and 41 are patentable over Ozcelik Ozcelik and Bublil in view of Chen and Mohamed and all claims depending therefrom are also necessarily patentable.


Accordingly, reconsideration and allowance of claims 27-47 is hereby respectfully requested. Applicants submit that the subject application is now in condition for allowance and an early notice thereof is requested. Should any further amendment be required prior to passing the application to issue, the Examiner is respectfully invited to contact the undersigned by telephone at the number set out below.

Respectfully submitted,

Dated: December 13, 2006  
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